

ABSTRACT OF THE DISCLOSURE

A tuner block in which a modulator and an IF/demodulator circuit are integrally formed. The tuner block has a casing for accommodating a tuner, the IF/ demodulator circuit and the modulator, and sixteen pins consecutively disposed at an outside of the casing. A number of the pins is reduced as compared with a conventional tuner block by supplying an electrical power to the modulator and the tuner through a common pin, dispensing with pins carrying unnecessary signals and pins which carry no signal and rearranging other pins. Potential for noise and interference between signals is reduced by maximizing displacements of connector pins which carry signals which are likely to interact. A switching arrangement provides for utilizing either a conventional tuner block or a tuner block according to the present invention on a circuit board.